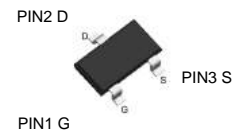


Description

The 5P01 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

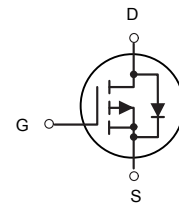


SOT23-3L

General Features

$V_{DS} = -18V$ $I_D = -5A$

$R_{DS(ON)} < -28m\Omega$ @ $V_{GS} = -10V$



P-Channel MOSFET

Application

Battery protection

Load switch

Uninterruptible power supply

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	-18	V
Gate-Source Voltage	V_{GS}	± 12	V
Drain Current -Continuous	I_D	-5.1	A
Drain Current -Pulsed (Note 1)	I_{DM}	-15	A
Maximum Power Dissipation	P_D	1.7	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 150	°C
Thermal Resistance, Junction-to-Ambient (Note 2)		$R_{\theta JA}$	

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA	-12	-18	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-12V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.45	-0.7	-1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-4.1A	-	28	45	mΩ
		V _{GS} =-2.5V, I _D =-3A	-	43	60	
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-2A	5	-	-	S
Input Capacitance	C _{iss}	V _{DS} =-4V, V _{GS} =0V, F=1.0MHz	-	740	-	PF
Output Capacitance	C _{oss}		-	290	-	PF
Reverse Transfer Capacitance	C _{rss}		-	190	-	PF
Turn-on Delay Time	t _{d(on)}	V _{DD} =-4V, I _D =-3.3A, R _L =- 1.2Ω, V _{GEN} =-4.5V, R _g =1Ω	-	12	-	nS
Turn-on Rise Time	t _r		-	35	-	nS
Turn-Off Delay Time	t _{d(off)}		-	30	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Q _g	V _{DS} =-4V, I _D =-4.1A, V _{GS} =-4.5V	-	7.8	-	nC
Gate-Source Charge	Q _{gs}		-	1.2	-	nC
Gate-Drain Charge	Q _{gd}		-	1.6	-	nC
Diode Forward Voltage ^(Note 3)	V _{SD}	V _{GS} =0V, I _S =-1.6A	-	-	-1.2	V
Diode Forward Current ^(Note 2)	I _S		-	-	4.1	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

Typical Performance Characteristics

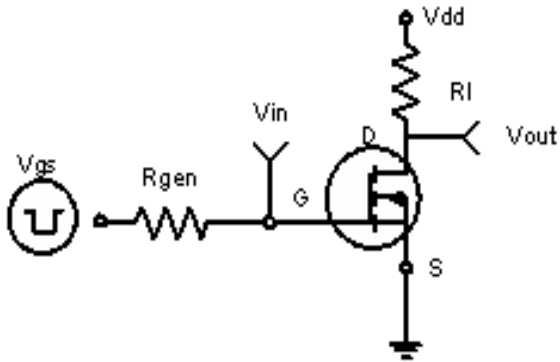


Figure 1: Switching Test Circuit

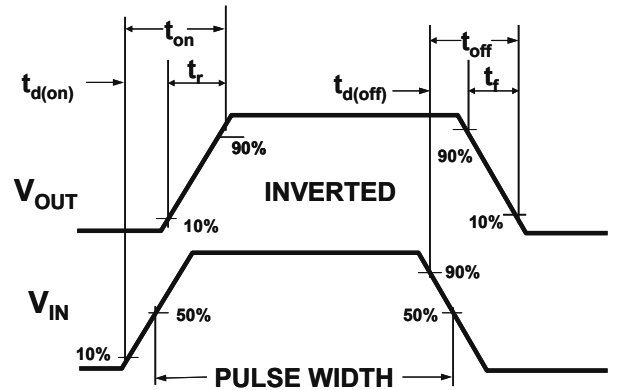


Figure 2: Switching Waveforms

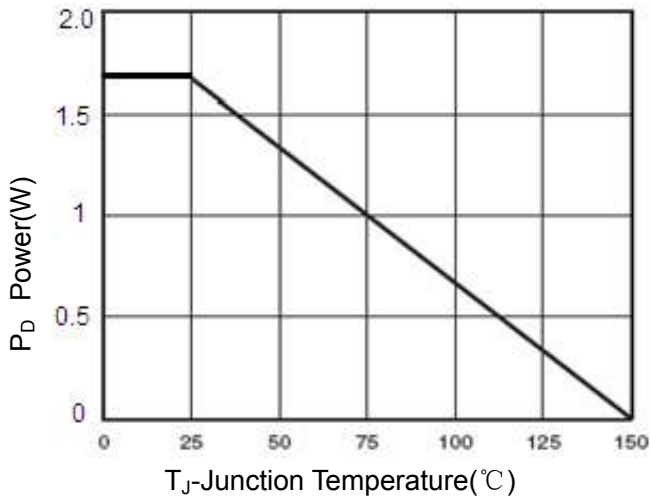


Figure 3 Power Dissipation

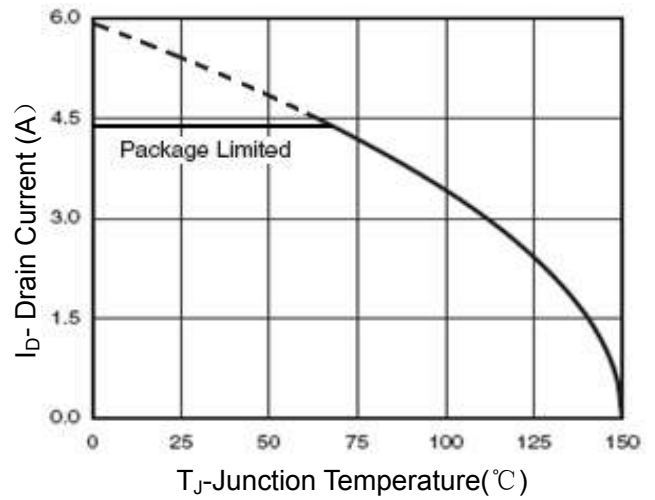


Figure 4 Drain Current

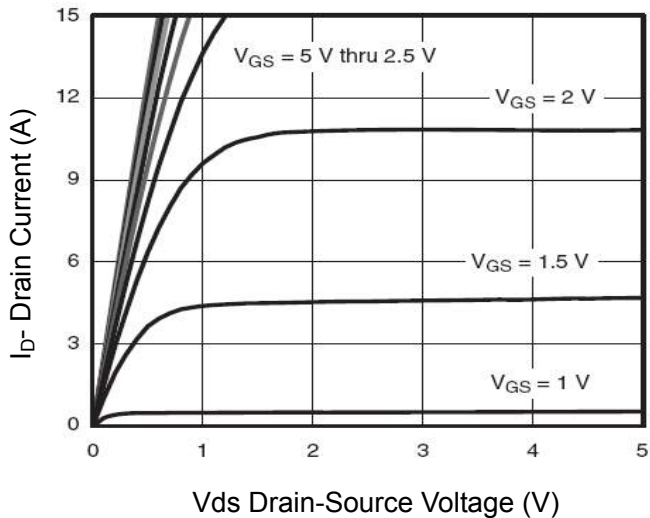


Figure 5 Output Characteristics

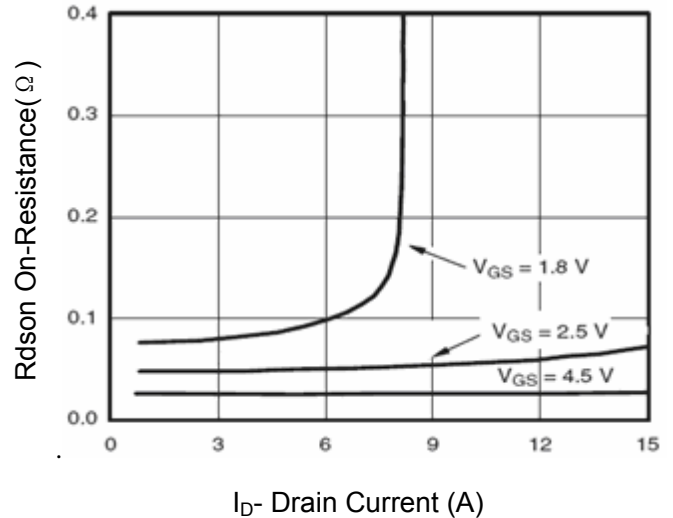


Figure 6 Drain-Source On-Resistance

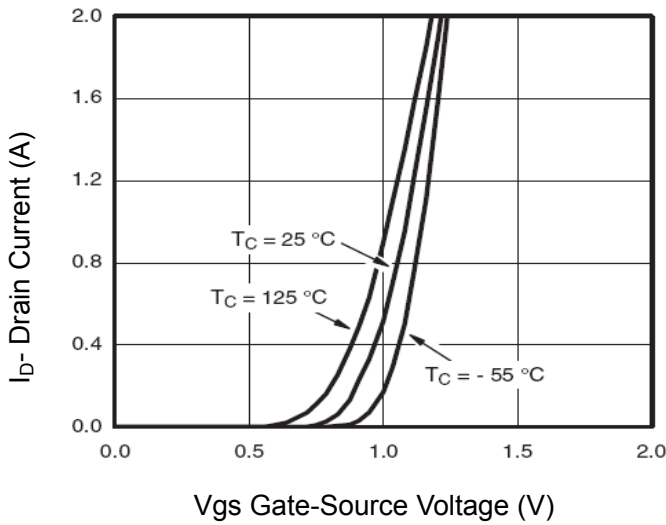


Figure 7 Transfer Characteristics

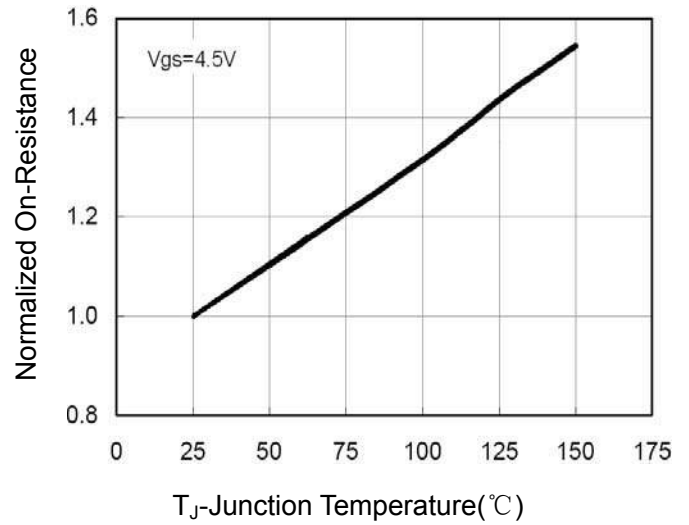


Figure 8 Drain-Source On-Resistance

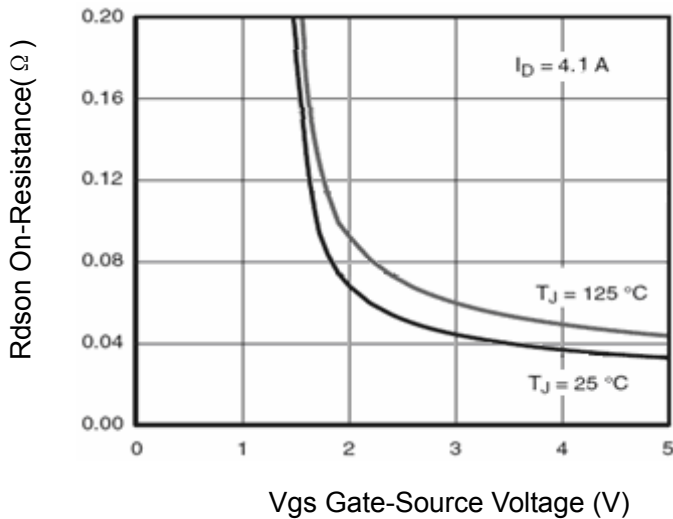


Figure 9 Rdson vs Vgs

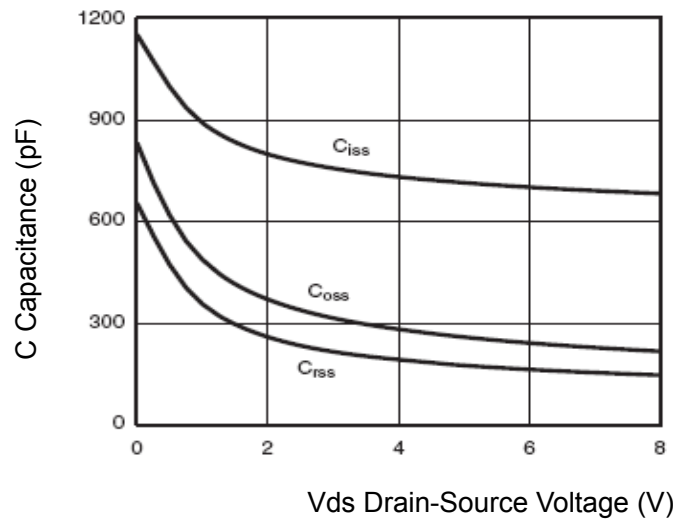


Figure 10 Capacitance vs Vds

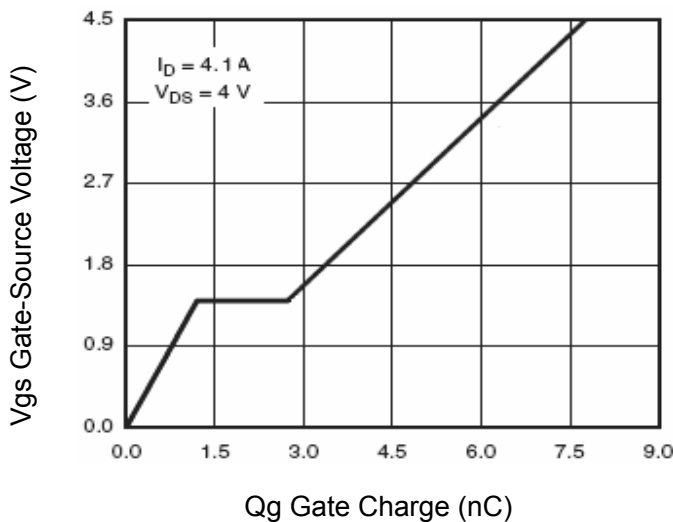


Figure 11 Gate Charge

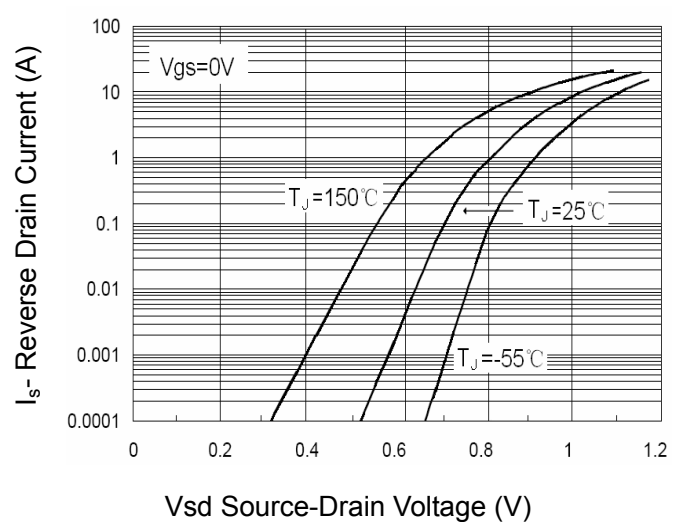


Figure 12 Source-Drain Diode Forward

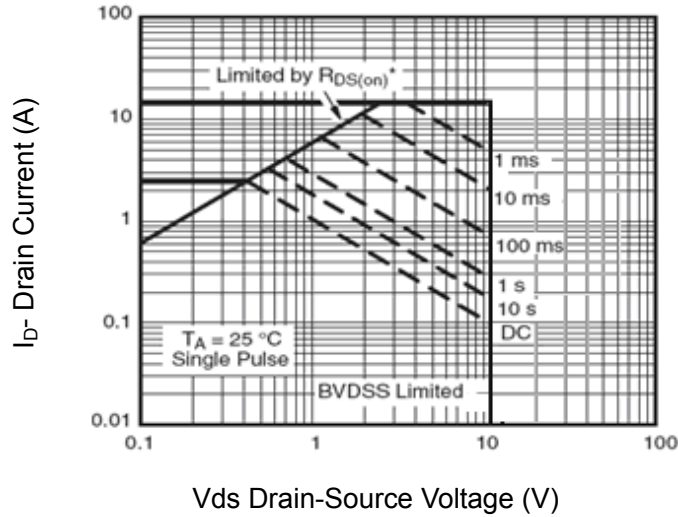


Figure 13 Safe Operation Area

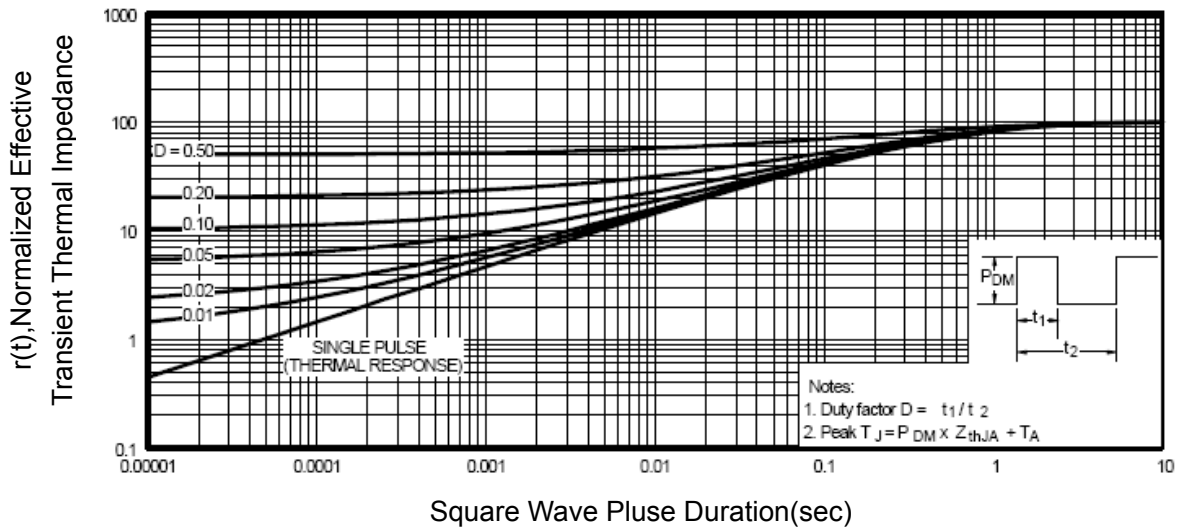
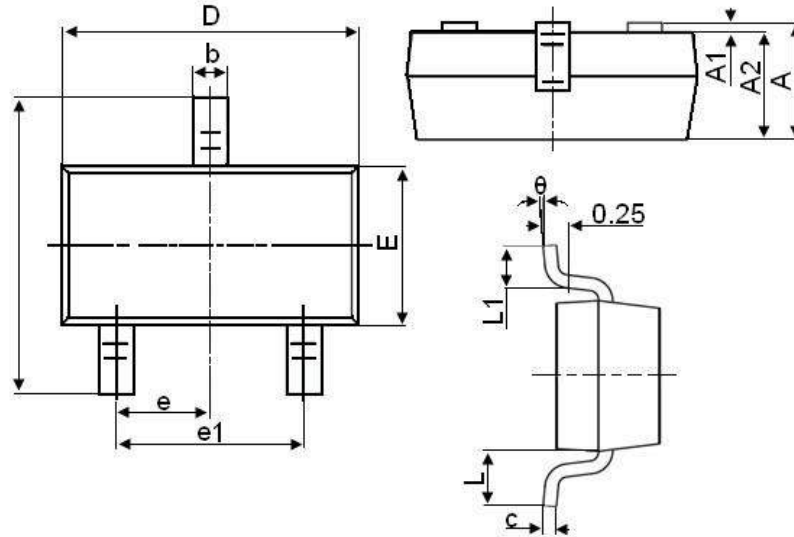


Figure 14 Normalized Maximum Transient Thermal Impedance

SOT23-3L Package Information


Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
c	0.100	0.200
D	2.800	3.000
E	1.500	1.700
E1	2.650	2.950
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.600
θ	0°	8°